

Interference Search History Printout

EAST Search History

	Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
Claims 22	S161	0	((built-in-self-test (built-in adj self-test) BIST ("first logic" adj (module section circuit device))) and (interface JTAG "boundary scan" ("second logic" adj (module section circuit device))) and ((((("third logic" adj (module section circuit device)) multiplex\$3 switch\$3) with (select\$5 near3 (connect\$3 coupl\$3))) with ((two plurality several multiple) near2 (control near2 signal))) and (\$2RAM memory SUT DUT CUT ((device chip circuit system) adj under adj test))).clm.	US-PGPUB	OR	ON	2008/04/11 14:49

4/11/2008 4:07:04 PM

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wsp